CLAIMS

What is claimed is:

1. An apparatus comprising:

a host processing system; and

a peripheral device comprising a storage medium comprising machine-readable instructions stored thereon for:

providing one or more programs capable of being hosted on the host processing system; and

initiating an agent to reside on the host processing system, the agent comprising logic to launch the one or more programs on the host processing in response to a predetermined event at the host processing system.

2. The apparatus of claim 1, wherein the one or more programs comprise an operating system and the agent comprises logic to initiate a system reset procedure of the host processing system in response to the predetermined event.

3. The apparatus of claim 2, wherein the agent comprises:

logic to map an address of an interrupt vector to a location in a storage medium storing machine-readable instructions for initiating a transmission of machine-readable instructions of one or more programs from the peripheral device to the host processing system; and

logic to initiate the system reset procedure, the system reset procedure comprising loading at least some of the transmitted instructions to a memory of the host processing system.

4. The apparatus of claim 2, wherein the one or more programs comprises a utility program and the agent further comprises logic to launch the utility program following a launch of the operating system in response to detection of the predetermined event.

5. The apparatus of claim 1, wherein the predetermined event comprises an event at a user interface of the host processing system.

6.   The apparatus of claim 1, wherein the apparatus further comprises a data bus coupled between the host processing system and the peripheral device, and wherein the peripheral device further comprises logic for transmitting machine-readable instructions to the host processing system for creating the agent in response to a procedure to enumerate the peripheral device on the bus.

7.   A method comprising:

initiating a transmission of machine-readable instructions from a peripheral device to a host processing system in response to a detection of a predetermined event at the host processing system;

executing at least some of the transmitted machine-readable instructions on the host processing system to launch one or more programs.

8.   The method of claim 7, wherein the one or more programs comprise an operating system and the method further comprises initiating a system reset procedure of the host processing system in response to the predetermined event to launch the operating system to the host processing system.

9.   The method of claim 8, wherein the method further comprises in response to detection of the predetermined event:

mapping an address of an interrupt vector to a location in a storage medium storing machine-readable instructions for initiating a transmission of machine-readable instructions of the one or more programs from the peripheral device to the host processing system; and

initiating the system reset procedure, the system reset procedure comprising loading at least some of the transmitted instructions to a memory of the host processing system.

10.   The method of claim 8, wherein the one or more programs comprises a utility program and the method further comprises launch the utility program following a launch of the operating system in response to detection of the predetermined event.

11.   The method of claim 7, wherein the predetermined event comprises an event at a user interface of the host processing system.

5   12.   The method of claim 7, wherein the method further comprises transmitting the machine-readable instructions from the peripheral device to the host processing system through a data bus coupled between the host processing system and the peripheral device contemporaneously with a procedure to enumerate the peripheral device on the data bus.

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13.   An article comprising:
a storage medium comprising machine-readable instructions stored thereon for:
        transmitting machine-readable instructions to a host processing system
        through a data bus, the machine readable instructions comprising instructions to
15   initiate hosting of an agent on the host processing system, the agent comprising
        logic to detect one or more predetermined events at the host processing system;
        and
        transmitting machine-readable instructions for launching one or more
        programs on the host processing system in response to a signal from the agent in
20   response to detection of one of the predetermined events.

14.   The article of claim 13, wherein the storage medium further comprises machine-readable instructions stored thereon for transmitting the machine-readable instructions for hosting the agent on the host processing system to the host processing
25   system contemporaneously with a procedure for enumerating the peripheral device on the data bus.

15.   The article of claim 13, wherein the one or more programs comprise an operating system and the agent comprises logic to initiate a system reset procedure of the
30   host processing system in response to the detection of the predetermined event to launch the operating system to the host processing system.

16.   The article of claim 15, wherein the agent comprises:

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logic to map an address of an interrupt vector to a location in a storage medium storing machine-readable instructions for initiating a transmission of machine-readable instructions of the operating system from the peripheral device to the host processing system; and

logic to initiate the system reset procedure, the system reset procedure comprising loading at least some of the transmitted instructions for the operating system to a memory of the host processing system.

17.   The article of claim 15, wherein the one or more programs comprise a utility program and the agent further comprises logic to launch the utility program following a launch of the operating system in response to detection of the predetermined event.

18.   The article of claim 13, wherein the predetermined event comprises an event at a user interface of the host processing system.

19.   A peripheral device comprising:

logic to transmit machine-readable instructions to a host processing system through a data bus, the machine readable instructions comprising instructions for hosting of an agent on the host processing system, the agent comprising logic to detect one or more predetermined events at the host processing system; and

logic to transmit machine-readable instructions for launching one or more programs on the host processing system in response to a signal from the agent generated in response to detection of one of the predetermined events.

20.   The peripheral device of claim 19, the peripheral device further comprising:

an interface to a data bus for transmitting data to the data bus; and

logic to transmit the machine-readable instructions for hosting the agent to the host processing system contemporaneously with a procedure for enumerating the peripheral device on the data bus.

21.   The peripheral device of claim 19, wherein the one or more programs comprise an operating system and the agent comprises logic to initiate a system reset procedure of the host processing system to launch the operating system to the host processing system in response to detection of the predetermined event.

22.   The peripheral device of claim 21, wherein the agent comprises:

logic to map an address of an interrupt vector to a location in a storage medium storing machine-readable instructions for initiating a transmission of machine-readable instructions for the operating system from the peripheral device to the host processing system; and

logic to initiate the system reset procedure, the system reset procedure comprising loading at least some of the transmitted instructions for the operating system to a memory of the host processing system.

23.   The peripheral device of claim 21, wherein the one or more programs comprise a utility program and the agent further comprises logic to launch the utility program following a launch of the operating system in response to detection of the predetermined event. ·

24.   The peripheral device of claim 19, wherein the predetermined event comprises an event at a user interface of the host processing system.

25.   An apparatus comprising:

means for initiating a transmission of machine-readable instructions from a peripheral device to a host processing system in response to a detection of a predetermined event at the host processing system;

means for executing at least some of the transmitted machine-readable instructions on the host processing system to launch one or more programs.

26.   The apparatus of claim 25, wherein the one or more programs comprise an operating system and the apparatus further comprises means for initiating a system reset

procedure of the host processing system in response to the predetermined event to launch the operating system to the host processing system.

27.   The apparatus of claim 26, the apparatus further comprising:

5   means for mapping an address of an interrupt vector to a location in a storage medium storing machine-readable instructions for initiating a transmission of machine-readable instructions of the one or more programs from the peripheral device to the host processing system in response to the predetermined event; and

means for initiating the system reset procedure, the system reset procedure

10   comprising loading at least some of the transmitted instructions to a memory of the host processing system.

28.   The apparatus of claim 26, wherein the one or more programs comprises a utility program and the apparatus further comprises means for launching the utility

15   program following a launch of the operating system in response to detection of the predetermined event.

29.   The apparatus of claim 25, wherein the predetermined event comprises an event at a user interface of the host processing system.

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30.   The apparatus of claim 25, the apparatus further comprising means for transmitting the machine-readable instructions from the peripheral device to the host processing system through a data bus coupled between the host processing system and the peripheral device contemporaneously with a procedure to enumerate the peripheral device

25   on the data bus.